IN THE SPECIFICATION:

Please amend the specification as follows.

On page 3, amend the single sentence paragraph at line 6 as follows:

FIG. 5 illustrates in block diagram form a phase lock loop of FIG. 4 <u>3</u>.

On page 14, amend the paragraph at lines 12-22 as follows:

Multiplier 330 receives the output of decimator 312 and the $\cos\theta$ phase correction signal and generates a product to be added to the negative product of the output of decimator 324 and $\sin\theta$. The generated sum provided by adder 350 is a first data component and is a phase corrected (L-R) signal so that the (L-R) signal is phase aligned with a second data component, the (L+R) signal. The output of adder 350 may be represented as:

$$(L-R)\cos\eta \times \cos\theta + (L-R)\sin\eta \times \sin\theta = (L-R)\cos(\eta-\theta)$$

Therefore, when θ exactly equals η indicating a lock condition for phase lock loop 376, the output of ~~multiplier 3650~~ <u>adder 350</u> is exactly (L-R).

On page 17, amend the paragraph at lines 4-7 as follows:

Predetermined coefficients are used by low pass filters 410 and 430 to implement different bandwidths. Whether low pass filters 410 and 430 are implemented by software or with hardware circuits, the coefficients can be ~~save~~ <u>saved</u> in tables or can be calculated in real time.